

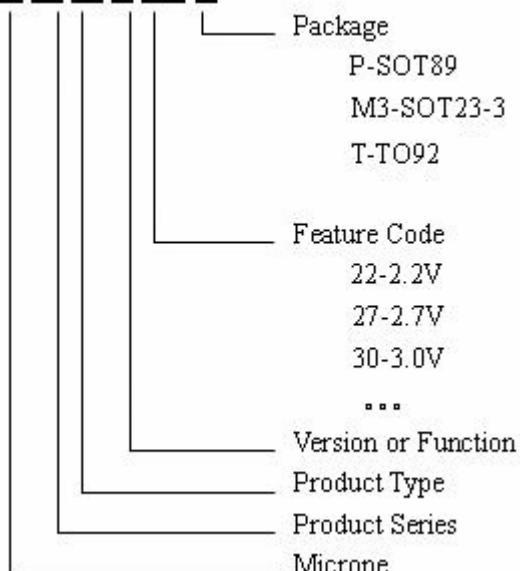
ME2802 Series Voltage Detectors

ME2802 Series are highly precise, low power consumption voltage detectors, manufactured using CMOS technologies. Detect voltage is extremely accurate with minimal temperature drift. NMOS output configurations are available.

Features

- Highly accurate: $\pm 1\%$;
- Low power consumption:
TYP 0.7uA ($V_{in}=1.5V$);
- Detect voltage range:
2.0V~4.8V in 0.1V increments;
- Operating voltage range: 0.7V~7V;
- Detect voltage temperature characteristics:
TYP $\pm 100\text{ppm}/^{\circ}\text{C}$;
- Output configuration: NMOS;
- PACKAGE: SOT23-3, SOT89-3, TO-92.

Selection Guide

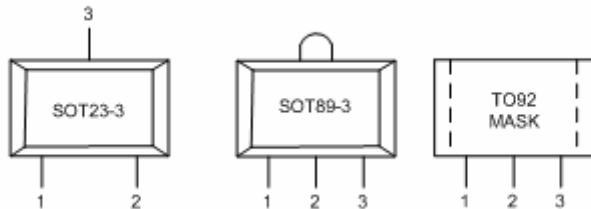
ME 28 02 A XX X


Applications

- Microprocessor reset circuitry;
- Memory battery back-up circuits;
- Power-on reset circuits;
- Power failure detection;
- System battery life and charge voltage monitors.

TYPE	POSTFIX	PACKAGE	OUTPUT CONFIGURATION
ME2802Axx	M3	SOT23-3	NMOS
	P	SOT89-3	
	T	TO-92	
	T1	TO-92	

Pin Configuration

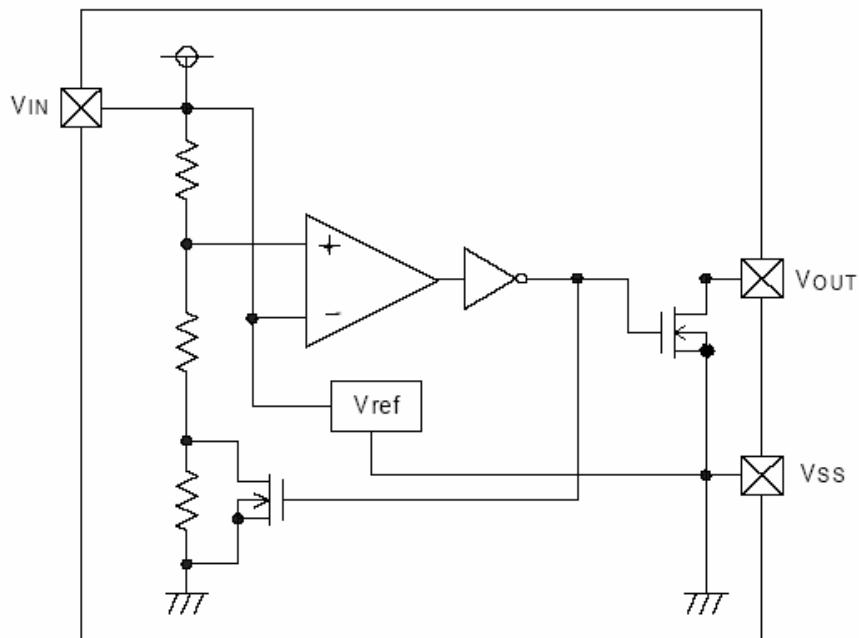


Pin Assignment

ME2802Axx

PIN Number				PIN NAME	FUNCTION
SOT23-3	SOT89-3	TO-92(T)	TO-92(T1)		
2	3	3	2	Vss	Ground
1	1	1	3	Vout	Output
3	2	2	1	Vin	Input

Block Diagram



Absolute Maximum Ratings

PARAMETER		SYMBAL	RATINGS	UNITS
V _{IN}	Input Voltage	V _{IN}	8	V
Output Current		I _{out}	50	mA
Output Voltage	NMOS	V _{out}	V _{ss} -0.3~V _{in} +0.3	V
	N-ch open drain		V _{ss} -0.3~12	
Continuous Total Power Dissipation	SOT23	P _d	150	mW
	SOT89		500	mW
	TO92		300	mW
Operating Ambient Temperature	T _{Opr}		-40~+85	°C
Storage Temperature	T _{stg}		-40~+125	°C
Soldering temperature and time	T _{solder}		260°C, 10s	

Electrical Characteristics (V_{DF}(T)=2.0V to 4.8V±1% Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V _{DF}	Detect Voltage		V _{DF} * 0.99	V _{DF}	V _{DF} * 1.01	V
V _{HYS}	Hysteresis Range		V _{DF} * 0.02	V _{DF} * 0.05	V _{DF} * 0.08	V
I _{ss}	Supply Current	Vin=1.5V =2.0V =3.0V =4.0V =5.0V	0.7	2.7		uA
			0.8	3.2		
			0.9	3.6		
			1.0	3.8		
			1.1	4.3		
V _{IN}	Operating Voltage	V _{DF} (T)=1.6V to 6.0V	0.7		7	V
I _{OUT}	Output Current	N-ch VDS=0.5V VIN=1.0V =2.0V =3.0V =4.0V =5.0V	1.0	2.2		mA
			3.0	7.7		
			5.0	10.1		
			6.0	11.5		
			7.0	13.0		
ΔV _{DF} /(Δtopr*V _{DF})	Temperature characteristics	-40°C≤Topr≤85°C		±100		ppm/°C

Note: 1、VDF(T) : Established Detect Voltage value

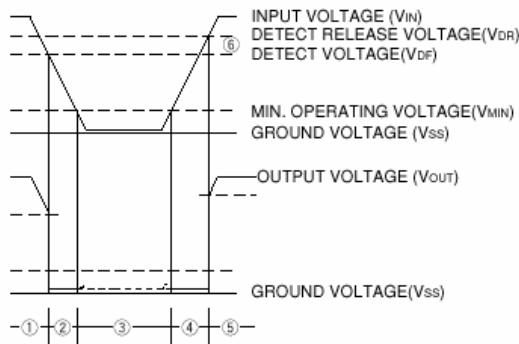
2、Release Voltage: V_{DR}=V_{DF}+V_{HYS}

Functional Description

Functional Description

- 1、 When input voltage (V_{IN}) rises above detect voltage (V_{DF}), output voltage (V_{OUT}) will be equal to V_{IN} .
- 2、 When input voltage (V_{IN}) falls below detect voltage (V_{DF}), output voltage (V_{OUT}) will be equal to the ground voltage (V_{SS}) level.
- 3、 When input voltage (V_{IN}) falls to a level below that of the minimum operating voltage (V_{MIN}), output will become unstable. In this condition, V_{IN} will equal the pulled-up output (should output be pulled-up.)
- 4、 When input voltage (V_{IN}) rises above the ground voltage (V_{SS}) level, output will be unstable at levels below the minimum operating voltage (V_{MIN}). Between the V_{MIN} and detect release voltage (V_{DR}) levels, the ground voltage (V_{SS}) level will be maintained.
- 5、 When input voltage (V_{IN}) rises above detect release voltage (V_{DR}), output voltage (V_{OUT}) will be equal to V_{IN} .
- 6、 The difference between V_{DR} and V_{DF} represents the hysteresis range.

Timing Chart

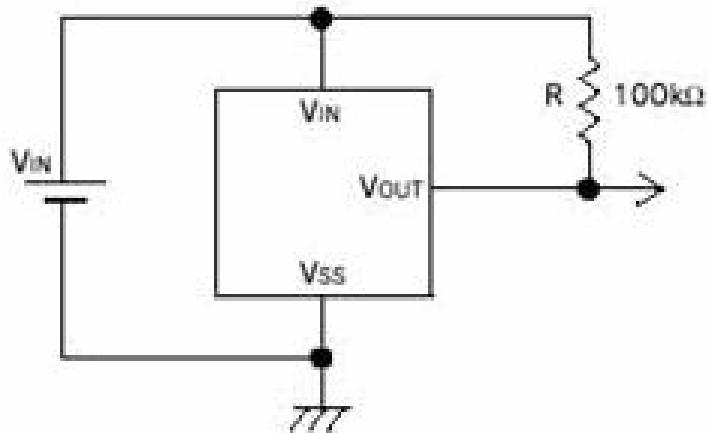


Directions for use

Notes on Use

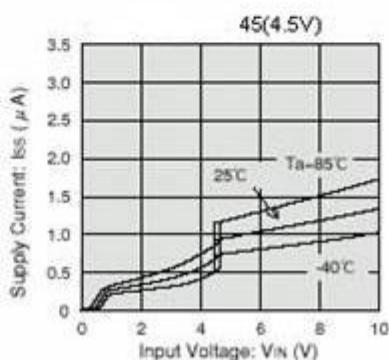
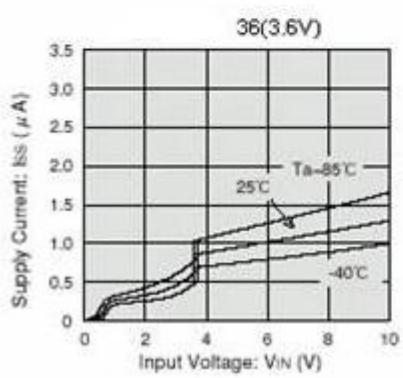
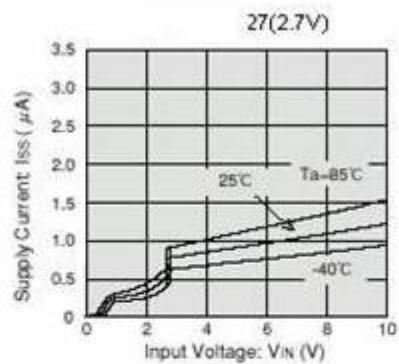
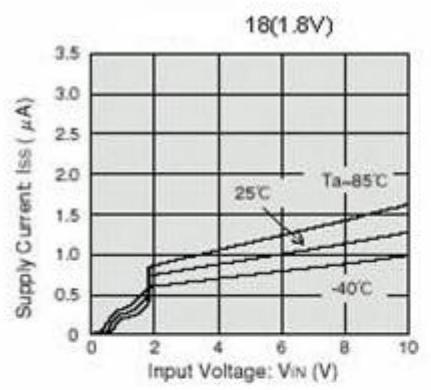
- 1、 Please use this IC within the stated maximum ratings. Operation beyond these limits may cause degrading or permanent damage to the device.
- 2、 In order to stabilize the IC's operations, please ensure that V_{IN} pin's input frequency's rise and fall times are more than several u Sec/V.

Typical Applications

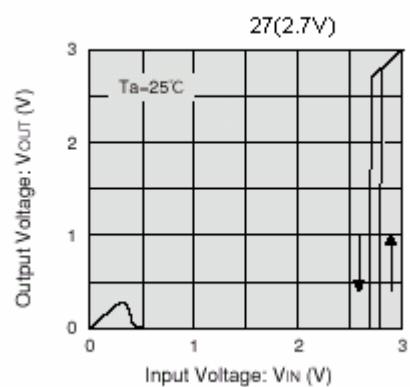
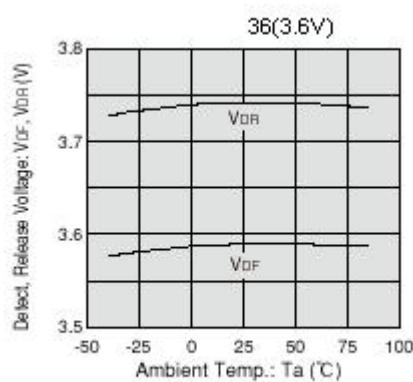
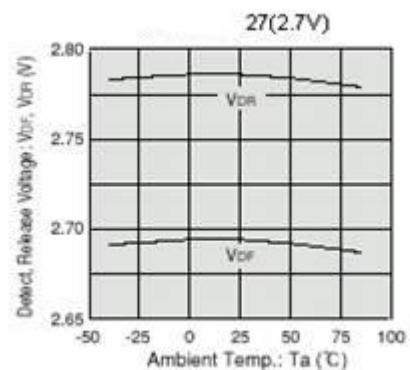
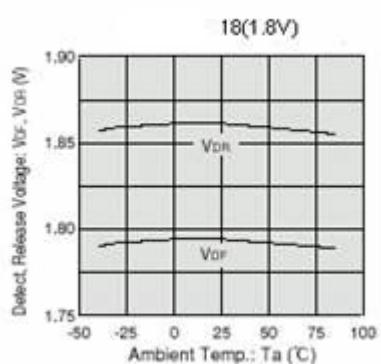


Type Characteristics

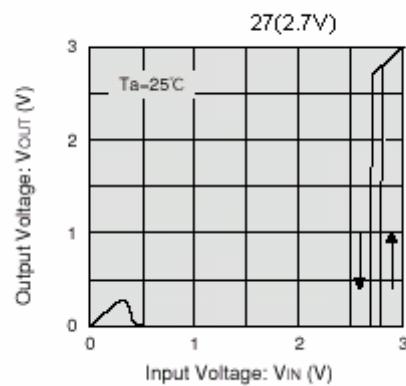
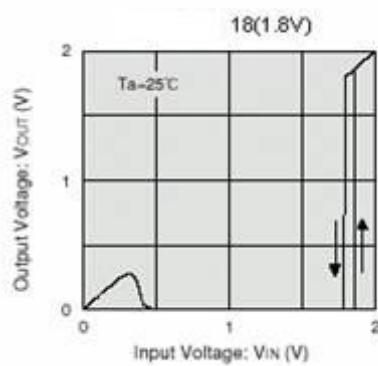
1. SUPPLY CURRENT vs. INPUT VOLTAGE

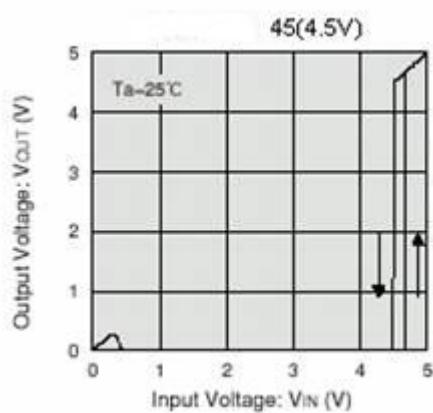
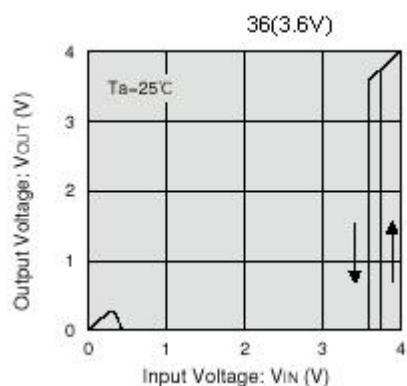


2. DETECT,RELEASE VOLTAGE vs. AMBIENT TEMPERATURE

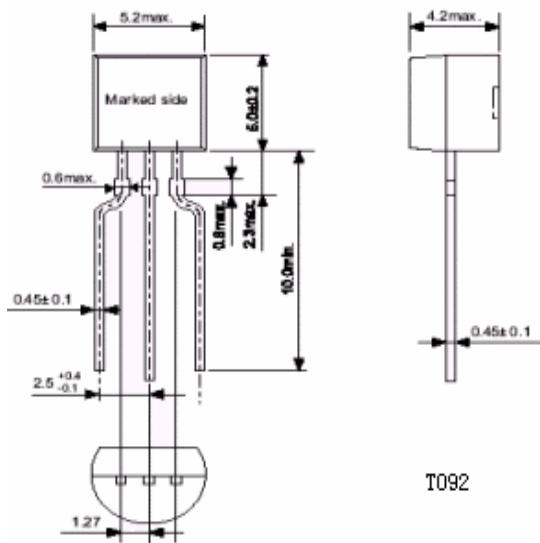
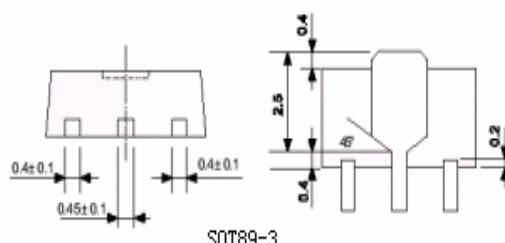
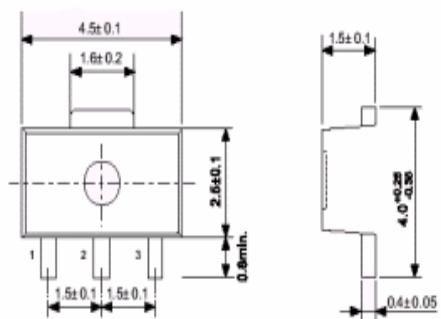
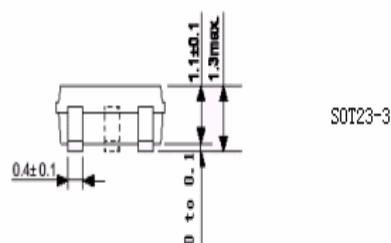
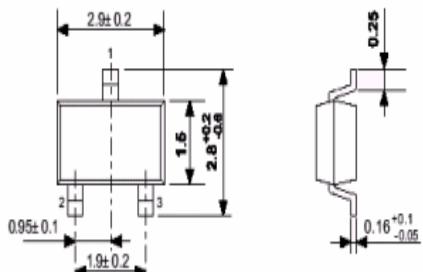


3. OUTPUT VOLTAGE vs. INPUT VOLTAGE





Package Dimensions



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