



Description

JMP N-channel MOSFET

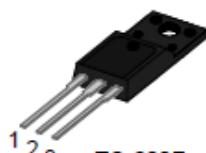
Features

- 800V,7A
- $R_{DS(ON)} = 1.35\Omega$ (Typ.) @ $V_{GS} = 10V$, $I_D = 3.5A$
- Fast Switching
- Improved dv/dt Capability
- 100% Avalanche Tested

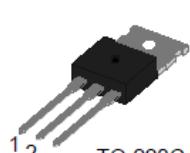
Application

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

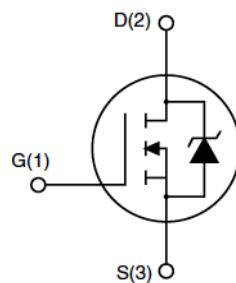
Package



JMPF7N80A



JMPC7N80A



Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.		Units
		TO-220C	TO-220F	
V_{DSS}	Drain-Source Voltage	800		V
V_{GSS}	Gate-Source Voltage	± 30		V
I_D	Continuous Drain Current $T_c = 25^\circ C$	7		A
		$T_c = 100^\circ C$	4.5	A
I_{DM}	Pulsed Drain Current ^{note1}	28		A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	245		mJ
E_{AR}	Repetitive Avalanche Energy	147		mJ
P_D	Power Dissipation $T_c = 25^\circ C$	70	25	W
R_{eJC}	Thermal Resistance, Junction to Case	1.78	5	$^\circ C/W$
R_{eJA}	Thermal Resistance, Junction to Ambient	60	62.5	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ C$

**Electrical Characteristics** ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	800	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=800V, V_{GS}=0V, T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{DS}=640V, V_{GS}=0V, T_J=125^\circ\text{C}$	-	-	100	nA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 30V$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	-	4.0	V
$R_{DS(\text{on})}$	Static Drain-Source on-Resistance note3	$V_{GS}=10V, I_D=3.5A$	-	1.35	1.6	Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{MHz}$	-	1178	-	pF
C_{oss}	Output Capacitance		-	128	-	pF
C_{rss}	Reverse Transfer Capacitance		-	27	-	pF
Q_g	Total Gate Charge	$V_{DD}=640V, I_D=7A, V_{GS}=10V$	-	49	-	nC
Q_{gs}	Gate-Source Charge		-	6	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	26	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=400V, I_D=7A, R_G=25\Omega$	-	43	-	ns
t_r	Turn-on Rise Time		-	28	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	244	-	ns
t_f	Turn-off Fall Time		-	54	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	7	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	28	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_{SD}=3.5A$	-	-	1.4	V
t_{rr}	Reverse Recovery Time	$V_{GS}=0V, I_s=7A, \text{di/dt}=100A/\mu\text{s}$	-	295	-	ns
Q_{rr}	Reverse Recovery Charge		-	1.7	-	μC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. $L=10\text{mH}, V_{DD}=50V, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1\%$

Typical Performance Characteristics

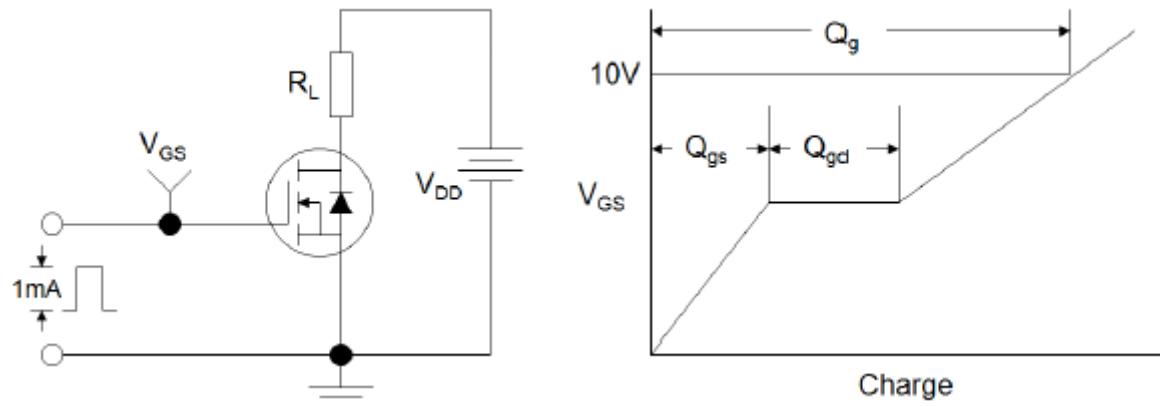


Figure 1: Gate Charge Test Circuit & Waveform

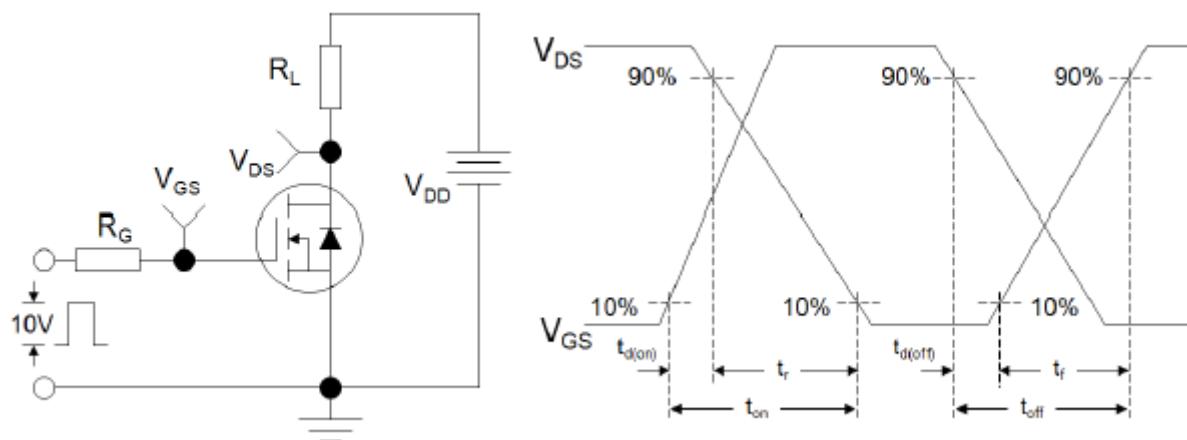


Figure 2: Resistive Switching Test Circuit & Waveforms

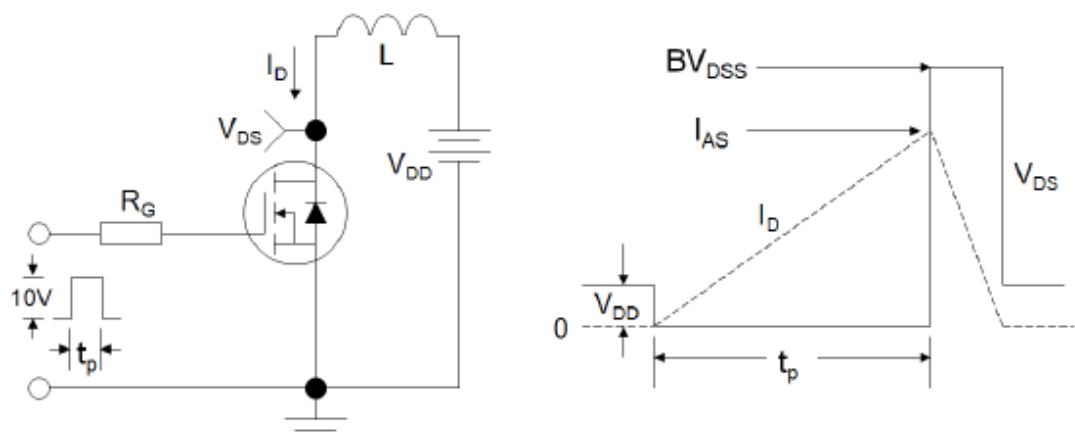
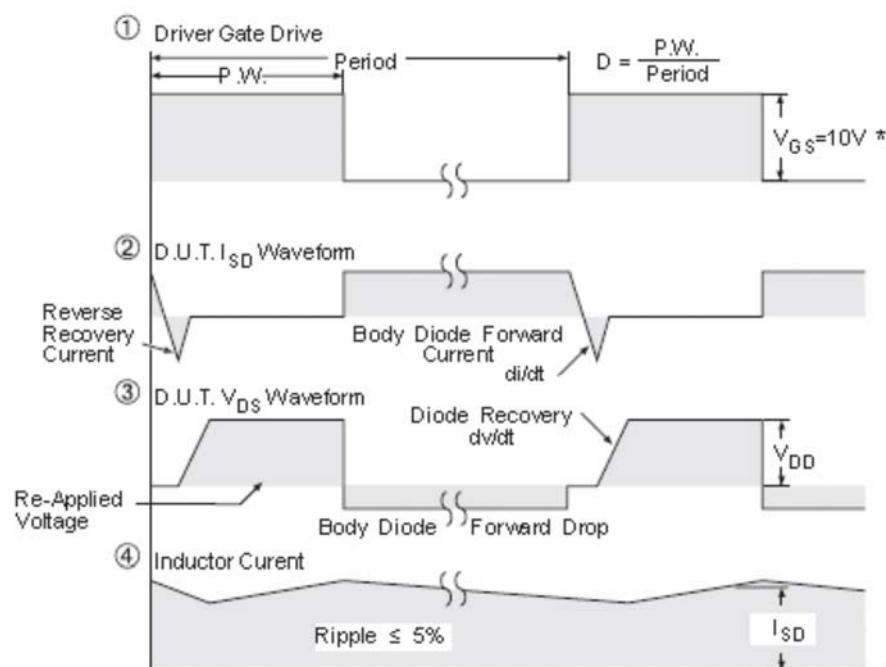
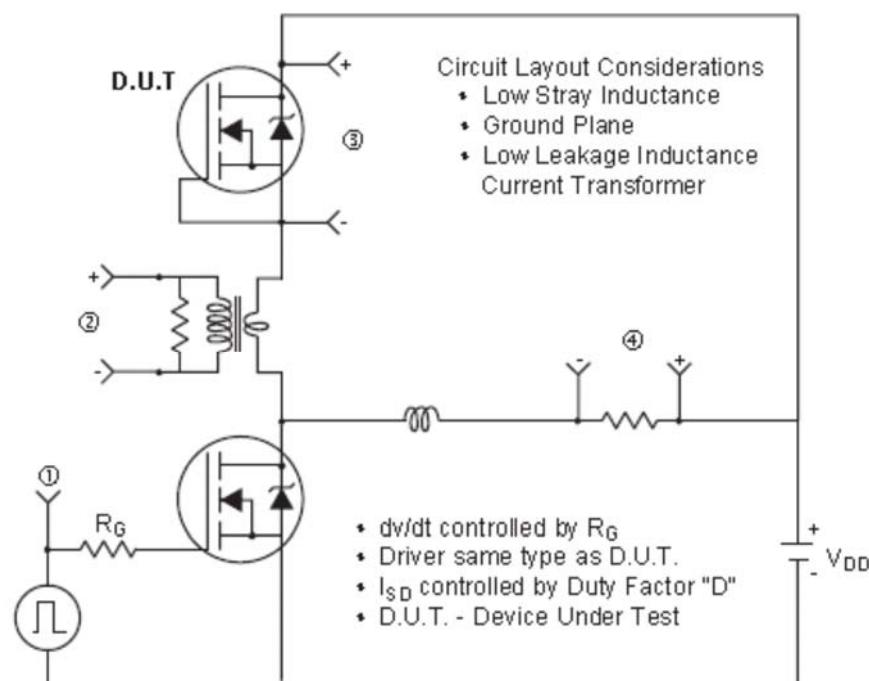


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

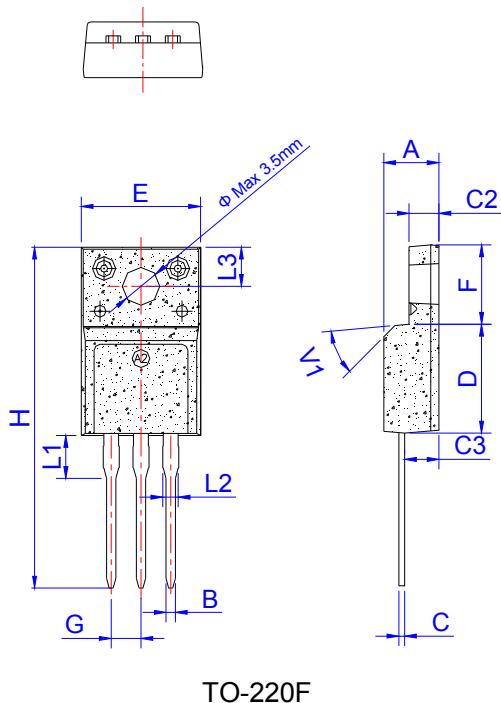


* $V_{GS} = 5V$ for Logic Level Devices

Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)



Package Mechanical Data



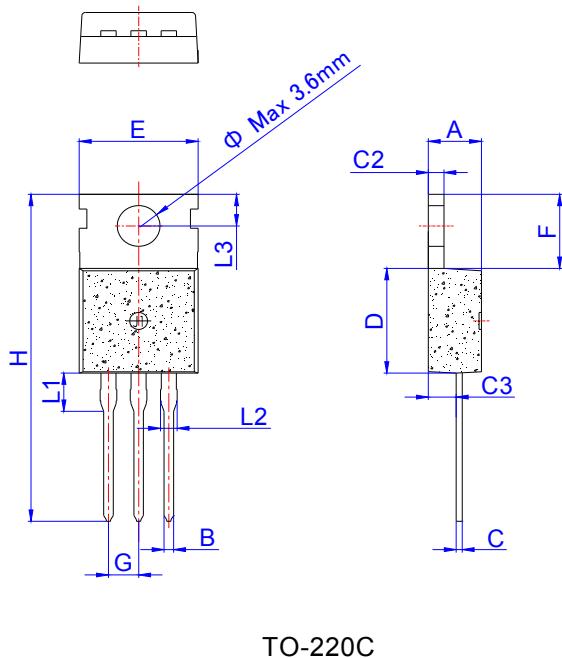
Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.63			0.143	
L2	1.14		1.70	0.045		0.067
L3		3.30			0.130	
V1		45°			45°	

Package Information -TO-220F

OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON (PCS)
TUBE	50	1,000	8,000



Package Mechanical Data



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		1.181
B	0.70		0.90	0.027		0.035
C	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.086		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
H	28.0		29.8	11.0		11.7
L1		3.39			0.133	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
Φ		3.6			0.142	

Package Information -TO-220C

OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON (PCS)
TUBE	50	1,000	8,000

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the first version which is made in 9-Mar.-2019. This document supersedes and replaces all information previously supplied.

JMP is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.
Copyright ©2019 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.