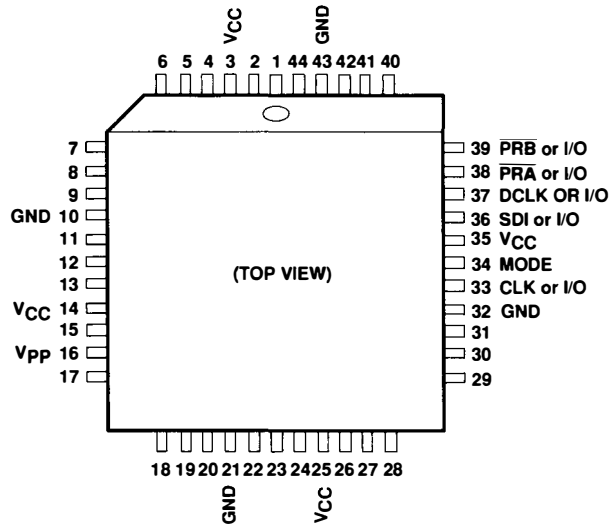


TPC10 SERIES CMOS FIELD-PROGRAMMABLE GATE ARRAYS

SRFS001F – D3864, DECEMBER 1989 – REVISED FEBRUARY 1993

package pin assignments

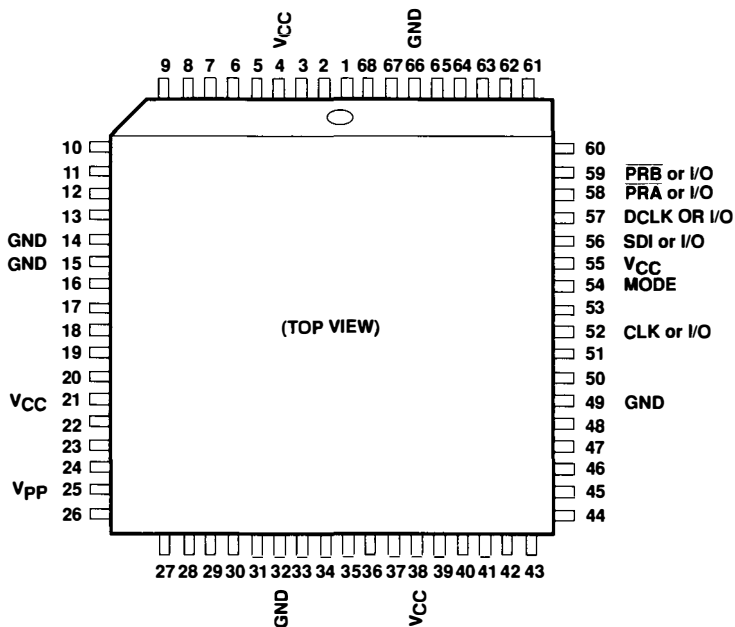


- NOTES: A. All pins marked GND are ground connections and must be connected to circuit ground
- B. V_{pp} must be terminated to V_{CC} except during programming.
- C. PRA and PRB, the diagnostic probe outputs, should remain open if not used as I/Os
- D. MODE must be terminated to circuit ground except during programming. †
- E. SDI and DCLK should be terminated to circuit ground during normal operation if not used as I/Os. †
- F. Unused I/O pins are automatically designated by the Action Logic System as outputs and should remain unconnected. Unused I/O pins are driven low by design.
- G. All unidentified pins on the pin assignment drawings are standard I/Os
- † The security fuse must be programmed for SDI and DCLK to function as I/Os. For device debugging on the user's circuit board, MODE, SDI, and DCLK should be terminated to circuit ground through a 10-k Ω (or greater) resistor. They can be tied to ground if not debugging

Figure 35. 44-Pin PLCC Pin Assignment

54LS387	23480	新环保批次	CDIP
54LS386J	23480	新环保批次	CDIP
54LS386JB	23480	新环保批次	CDIP14
54LS386AJ	23480	新环保批次	
74LS95N	23480	新环保批次	DIP
54S279/BEAJC	23480	新环保批次	CDIP
AT074	23480	新环保批次	SSOP
2S119	23480	新环保批次	CAN3
LM3310SQX/NOPB	23480	新环保批次	24-WQFN
UC2829ADW	23480	新环保批次	SOIC-16
PGY	23480	新环保批次	SOT23-5
TL064NSR	23480	新环保批次	SOP14-5.2
TL064NS	23480	新环保批次	SMD
TL064N	23480	新环保批次	DIP
LM3310	23480	新环保批次	
HJR1-2C-L-24VDC	23480	新环保批次	DIP
HJR1-2C-L-5VDC	23480	新环保批次	DIP
HJR1-2C-L-5V	23480	新环保批次	DIP
HJR1-2C-N-05V(104)	23480	新环保批次	
HJR1-2C-05V	23480	新环保批次	DIP
HJR1-2CL-3V	23480	新环保批次	
HJR1-2CL-5V	23480	新环保批次	
HJR1-2CL-48V	23480	新环保批次	
HJR1-2CN-05V(104)	23480	新环保批次	
HJR1-2CL-5VDC	23480	新环保批次	DIP
HJR1-2CL-DC5V	23480	新环保批次	DIP
HJR1-2C-L-DC5V	23480	新环保批次	DIP
HJR1-2C-L-DC12V	23480	新环保批次	DIP
HJR1-2C-M-5V	23480	新环保批次	RELAY
HJR1-2CL-9V	23480	新环保批次	
HJR1-2CL-6V	23480	新环保批次	
HJR1-2C 5V	23480	新环保批次	DIP

package pin assignments (continued)



- NOTES: A. All pins marked GND are ground connections and must be connected to circuit ground.
 B. V_{PP} must be terminated to V_{CC} except during programming.
 C. \overline{PRA} and \overline{PRB} , the diagnostic probe outputs, should remain open if not used as I/Os.
 D. MODE must be terminated to circuit ground except during programming.†
 E. SDI and DCLK should be terminated to circuit ground during normal operation if not used as I/Os.†
 F. Unused I/O pins are automatically designated by the Action Logic System as outputs and should remain unconnected. Unused I/O pins are driven low by design.
 G. All unidentified pins on the pin assignment drawings are standard I/Os
- † The security fuse must be programmed for SDI and DCLK to function as I/Os. For device debugging on the user's circuit board, MODE, SDI, and DCLK should be terminated to circuit ground through a 10-k Ω (or greater) resistor. They can be tied to ground if not debugging.

Figure 36. 68-Pin PLCC Pin Assignment

HJR1-2CL-6V	23480	新环保批次	
HJR1-2C 5V	23480	新环保批次	DIP
HJR1-2C 24V	23480	新环保批次	DIP
HJR1-2CM-5VDC	23480	新环保批次	
F642168APBE	23480	新环保批次	QFP
OPNI	23480	新环保批次	MSOP-8
5962-8776001SSA=SNV54ACT244W	23480	新环保批次	CFP20
SFC2205C	23480	新环保批次	
LS592	23480	新环保批次	SOP16
SFC493PM	23480	新环保批次	
SFC4112PM	23480	新环保批次	
SFC-402PM	23480	新环保批次	
VSP1900DBT	23480	新环保批次	SSOP
VSP1900DBTR	23480	新环保批次	TSSOP30
VSP1900DBTRG4	23480	新环保批次	TSSOP30
VSP1900DBTG4	23480	新环保批次	TSSOP-30
VSP1900BT	23480	新环保批次	TSSOP
TLE2071AMFKB	23480	新环保批次	LCCC20
5962-9460202Q2A TLE2072MFKB	23480	新环保批次	LCC20
5962-9080702Q2A TLE2061AMFKB	23480	新环保批次	LCC20
LP8720TLE-B	23480	新环保批次	20-WFBGA
PT6948A	23480	新环保批次	SIP
MSP430F2012IPW	23480	新环保批次	TOSSOP
BC244	23480	新环保批次	SSOP24
CD4203UBF3A	23480	新环保批次	CDIP14
LBC172A16DW	23480	新环保批次	SOP-16
TL1494CNS	23480	新环保批次	SOP16
TL1494CNSR	23480	新环保批次	SOP5.2
TL1494CD	23480	新环保批次	SOP
TL1494CN	23480	新环保批次	DIP-16
TL1494NSR	23480	新环保批次	SOP16
BC2048	23480	新环保批次	QFN