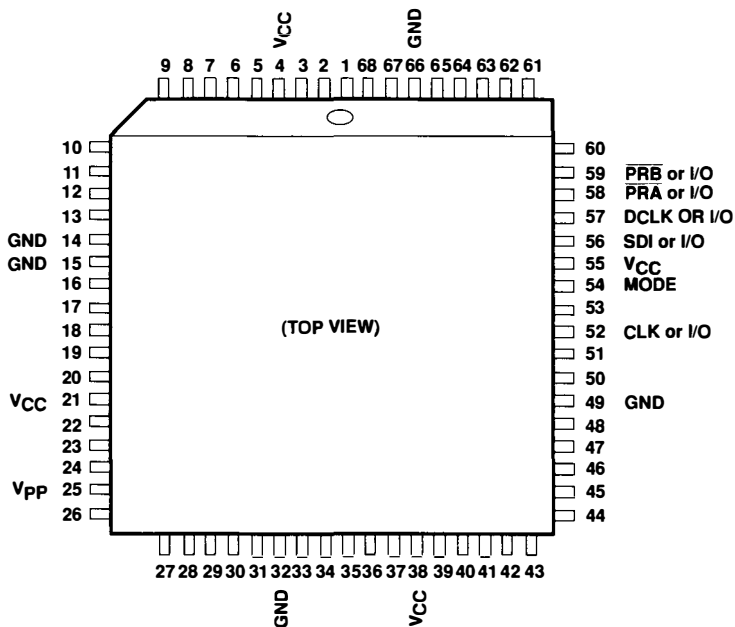


package pin assignments (continued)



- NOTES: A. All pins marked GND are ground connections and must be connected to circuit ground.
 B. V_{PP} must be terminated to V_{CC} except during programming.
 C. \overline{PRA} and \overline{PRB} , the diagnostic probe outputs, should remain open if not used as I/Os.
 D. MODE must be terminated to circuit ground except during programming.†
 E. SDI and DCLK should be terminated to circuit ground during normal operation if not used as I/Os.†
 F. Unused I/O pins are automatically designated by the Action Logic System as outputs and should remain unconnected. Unused I/O pins are driven low by design.
 G. All unidentified pins on the pin assignment drawings are standard I/Os
- † The security fuse must be programmed for SDI and DCLK to function as I/Os. For device debugging on the user's circuit board, MODE, SDI, and DCLK should be terminated to circuit ground through a 10-k Ω (or greater) resistor. They can be tied to ground if not debugging.

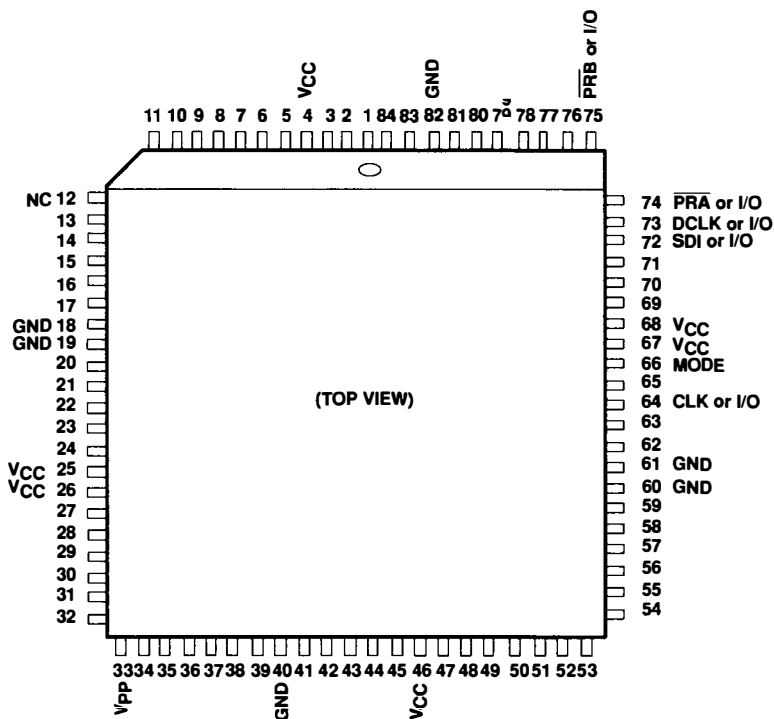
Figure 36. 68-Pin PLCC Pin Assignment

V62/06670-03XE	23480	新环保批次	
V62/04660-02YE	23480	新环保批次	
V62/03625-04YE	23480	新环保批次	SOIC-8
V62/03630-04XE	23480	新环保批次	HTSSOP-20
V62/03624-05XE	23480	新环保批次	Tape
V62/03648-01XE	23480	新环保批次	Tape
V62/03647-01XE	23480	新环保批次	Tape
V62/06630-01XE	23480	新环保批次	Tape
V62/03604-01XE	23480	新环保批次	Tape
V62/03616-01XE	23480	新环保批次	PQFP
V62/06644-03XE	23480	新环保批次	SOT23-5
V62/11620-01XE	23480	新环保批次	SOT23-5
V62/03628-01XE	23480	新环保批次	SOIC8
V62/03615-01XE	23480	新环保批次	SOIC20
V62/03609-01XE	23480	新环保批次	TSSOP32
V62/06602-01XE	23480	新环保批次	TSSOP16
V62/03634-03XE	23480	新环保批次	SOT23-5
V62/03618-02UE	23480	新环保批次	TSSOP14
V62/04610-01XE	23480	新环保批次	LQFP-144
V62/03625-03YE	23480	新环保批次	AN
V62/03671-02XE	23480	新环保批次	SOP
V62/03602-01XE	23480	新环保批次	NA
54L74	23480	新环保批次	CDIP
HTS02A	23480	新环保批次	SOP8
MF4A-50CP	23480	新环保批次	DIP8
MF4A-50IP	23480	新环保批次	DIP8
MF4A-50ID	23480	新环保批次	SOP8
MF4A-50MP	23480	新环保批次	DIP8
MF4A-50CD	23480	新环保批次	SOP8
A1405A	23480	新环保批次	SOP8
FE8900	23480	新环保批次	DIP16
PAPI	23480	新环保批次	SOT23-5

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package pin assignments (continued)



- NOTES: A. All pins marked GND are ground connections and must be connected to circuit ground
 B. V_{PP} must be terminated to V_{CC} except during programming.
 C. PRA and PRB, the diagnostic probe outputs, should remain open if not used as I/Os.
 D. MODE must be terminated to circuit ground except during programming.†
 E. SDI and DCLK should be terminated to circuit ground during normal operation if not used as I/Os.†
 F. Unused I/O pins are automatically designated by the Action Logic System as outputs and should remain unconnected. Unused I/O pins are driven low by design.
 G. All unidentified pins on the pin assignment drawings are standard I/Os
- † The security fuse must be programmed for SDI and DCLK to function as I/Os. For device debugging on the user's circuit board, MODE, SDI, and DCLK should be terminated to circuit ground through a 10-k Ω (or greater) resistor. They can be tied to ground if not debugging

Figure 37. 84-Pin PLCC Pin Assignment

FE8900	23480	新环保批次	DIP16
PAPI	23480	新环保批次	SOT23-5
4F2121	23480	新环保批次	TSSOP20
4F2121T	23480	新环保批次	SSOP-20
4F2121I	23480	新环保批次	TSSOP20
TS1871ILT TEL	23480	新环保批次	SOT23-5
SJ3B	23480	新环保批次	SOT23-6
TCT1004	23480	新环保批次	DIPSMD
TCT1003	23480	新环保批次	
TL102IP	23480	新环保批次	DIP-8
1408G4	23480	新环保批次	TSSOP-56
SN74HCT273DBR HT273	23480	新环保批次	
74HCT273APWR(HT273)	23480	新环保批次	TSSOP
SN27505YZGR	23480	新环保批次	BGA
SN27505YZGT-R1	23480	新环保批次	BGA
SN27505YZGR-R3	23480	新环保批次	BGA
SN27505	23480	新环保批次	BGA
SN27500DRZR-070	23480	新环保批次	QFN
SN27500DRZT-I1	23480	新环保批次	
TCL7733IDR	23480	新环保批次	SOP-8
C0DC2MI0PGER	23480	新环保批次	TQFP
C0DHSPDDCPR	23480	新环保批次	TSSOP
C0DBAYY02668	23480	新环保批次	
XCB3T16211	23480	新环保批次	TSSOP
CAG	23480	新环保批次	QFN-16
A1Y	23480	新环保批次	SC70-6
ADS1192IRSMR	23480	新环保批次	VQFN32
ADS1192IRSMT	23480	新环保批次	QFN32
ADS1196CZXGR	23480	新环保批次	NFBGA
ADS1196CZXGT	23480	新环保批次	NFBGA
ADS1194CZXGR	23480	新环保批次	NFBGA
ADS1196CPAGR	23480	新环保批次	TQFP-64