

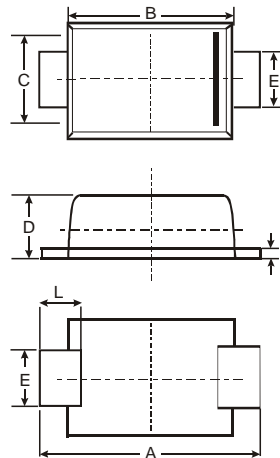
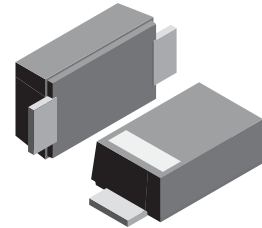
**VOLTAGE RANGE: 50 - 600V**  
**CURRENT: 3.0 A**

### Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop, High Efficiency
- Low Power Loss
- Super-Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-0

### Mechanical Data

- Case: SMBF , Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.0018 ounces, 0.05grams



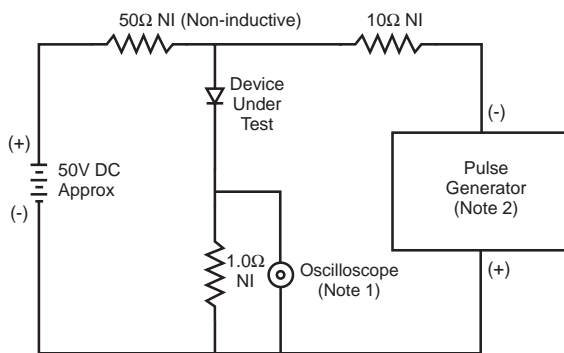
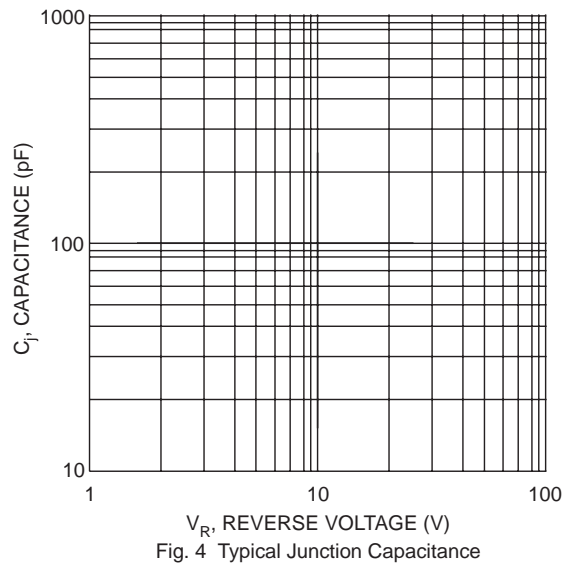
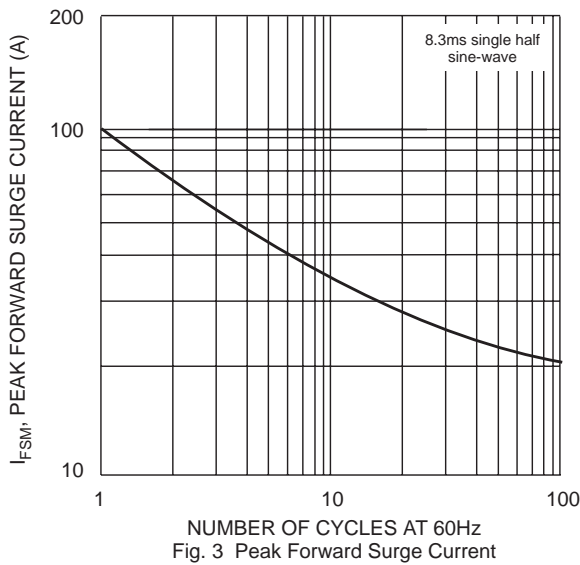
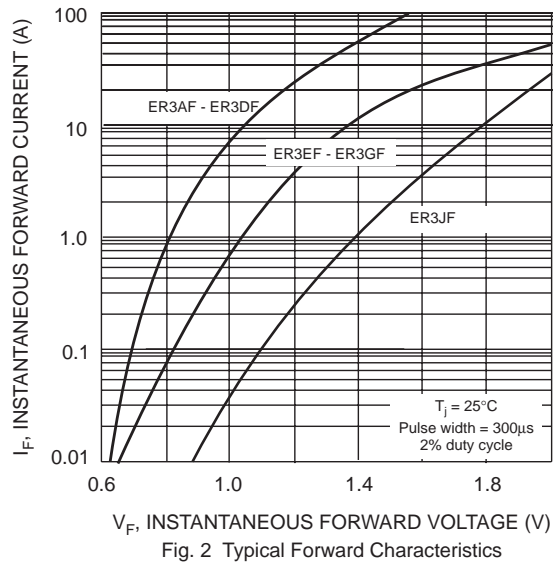
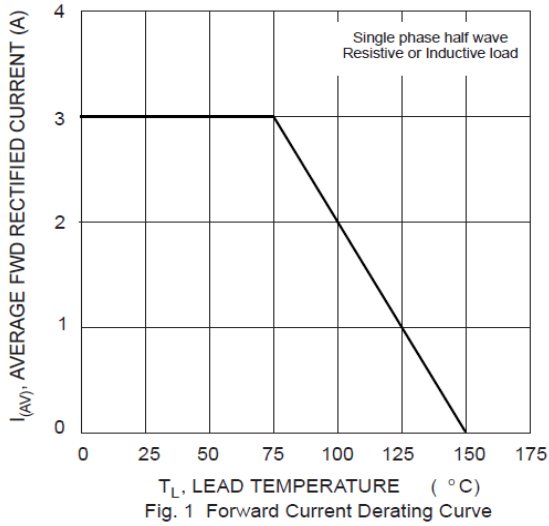
SMBF			
Dim	Min	Max	Typ
A	5.45	5.55	5.50
B	4.27	4.33	4.30
C	3.57	3.63	3.60
D	1.32	1.38	1.35
E	1.96	2.00	1.98
H	0.019	0.021	0.20
L	0.73	0.77	0.75
All Dimensions in mm			

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

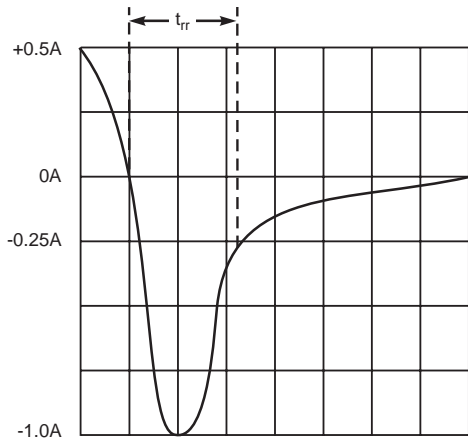
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	ER3AF	ER3BF	ER3CF	ER3DF	ER3EF	ER3GF	ER3JF	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>								
Working Peak Reverse Voltage	V <sub>RWM</sub>	50	100	150	200	300	400	600	V
DC Blocking Voltage	V <sub>R</sub>								
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	105	140	210	280	420	V
Average Rectified Output Current @T <sub>L</sub> = 75°C	I <sub>O</sub>	3.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	100							A
Forward Voltage @I <sub>F</sub> = 3.0A	V <sub>FM</sub>	0.95				1.25		1.7	V
Peak Reverse Current @T <sub>A</sub> = 25°C	I <sub>RM</sub>	5.0							μA
At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C		500							
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	35							nS
Typical Junction Capacitance (Note 2)	C <sub>j</sub>	45							pF
Typical Thermal Resistance (Note 3)	R <sub>θJL</sub>	16							°C/W
Operating and Storage Temperature Range	T <sub>i</sub> , T <sub>STG</sub>	-65 to +150							°C

Note: 1. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>rr</sub> = 0.25A. See figure 5.  
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.  
 3. Mounted on P.C. Board with 8.0mm<sup>2</sup> land area.



- Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
  2. Rise Time = 10ns max. Input Impedance = 50Ω.



Set time base for 5/10ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit