

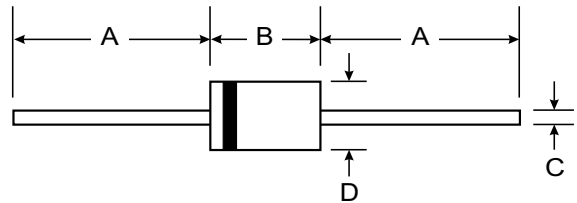
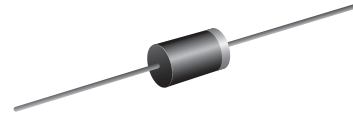
**VOLTAGE RANGE: 200 - 800V**  
**CURRENT: 2.0 A**

### Features

- Low cost
- Diffused junction
- Glass passivated chips
- Low forward voltage drop
- High current capability
- Easily cleaned with Freon, Alcohol, Isopropanol and similar solvents

### Mechanical Data

- Case : DO-15 Molded plastic
- Epoxy : UL94V-O rate flame retardant
- Lead : Axial lead solderable per MIL-STD-202, Method 208 guaranteed
- Polarity : Color band denotes cathode end
- Mounting position : Any
- Weight : 0.465 gram



DO-15		
Dim	Min	Max
A	25.40	—
B	5.50	7.62
C	0.686	0.889
D	2.60	3.60
All Dimensions in mm		

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

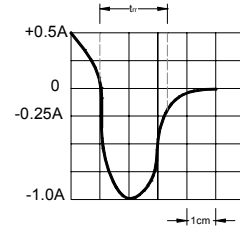
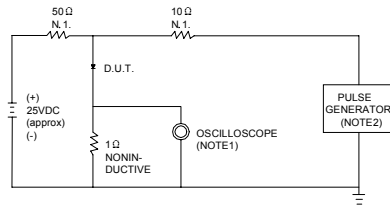
Characteristic	Symbol	1N5059	1N5060	1N5061	1N5062	Unit
Maximum recurrent peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	V
Maximum RMS voltage	V <sub>RMS</sub>	140	280	420	560	V
Maximum DC blocking voltage	V <sub>DC</sub>	200	400	600	800	V
Maximum average forward rectified current 9.5mm lead length, @T <sub>A</sub> =50°C	I <sub>F(AV)</sub>	2.0				A
Peak forward surge current 10ms single half-sine-wave superimposed on rated load @T <sub>J</sub> =125°C	I <sub>FSM</sub>	50.0				A
Maximum instantaneous forward voltage @1.0A @2.5A	V <sub>F</sub>	1.0 1.15				V
Maximum reverse current @T <sub>A</sub> =25°C at rated DC blocking voltage @T <sub>A</sub> =150°C	I <sub>R</sub>	1.0 100				μA
Maximum reverse recovery time (Note1)	t <sub>rr</sub>	4.0				μs
Typical junction capacitance (Note2)	C <sub>J</sub>	40				pF
Typical thermal resistance (Note3)	R <sub>θJA</sub>	45				K/W
Operating junction temperature range	T <sub>J</sub>	- 55 ----- + 175				°C
Storage temperature range	T <sub>STG</sub>	- 55 ----- + 175				°C

NOTE: 1. Measured with I<sub>F</sub>=0.5A, I<sub>R</sub>=1A, I<sub>rr</sub>=0.25A.

2. Measured at 1.0MHz and applied reverse voltage of 0V DC.

3. Thermal resistance from junction to ambient.

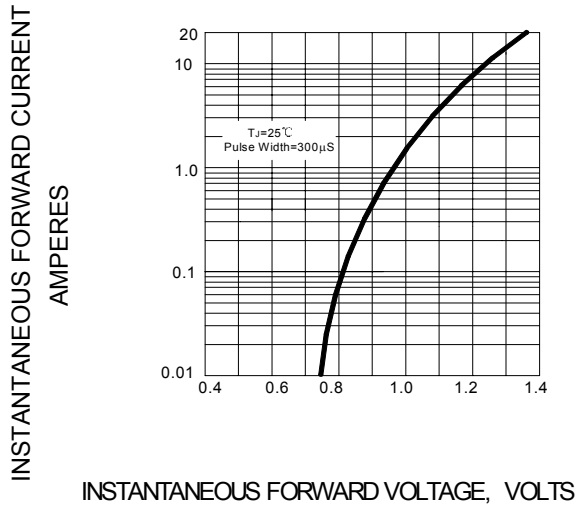
**FIG.1 – TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC**



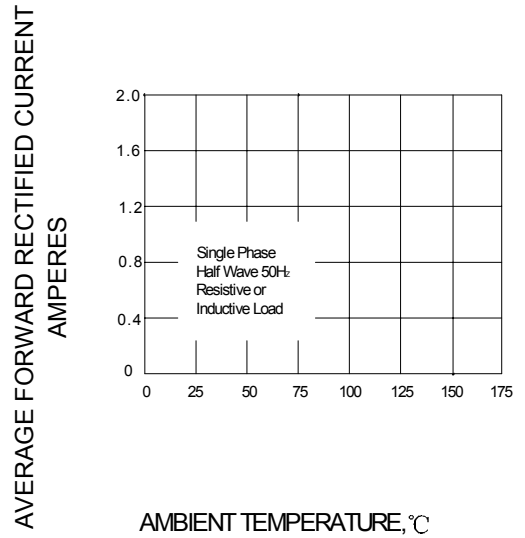
NOTES: 1. RISE TIME = 7ns MAX INPUT IMPEDANCE = 1MΩ. 22pF.  
 2. RISE TIME = 10ns MAX SOURCE IMPEDANCE = 50 Ω.

SET TIME BASE FOR 2.0 μ s/cm

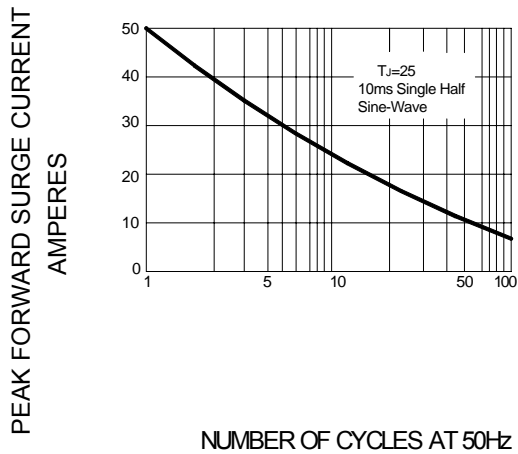
**FIG.2 – TYPICAL FORWARD CHARACTERISTIC**



**FIG.3 – FORWARD DERATING CURVE**



**FIG.4 – PEAK FORWARD SURGE CURRENT**



**FIG.5 – TYPICAL JUNCTION CAPACITANCE**

