

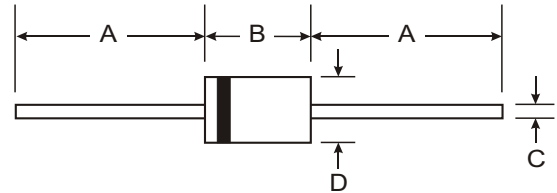
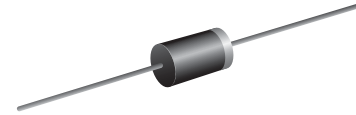
VOLTAGE RANGE: 50 - 1000V
CURRENT: 1.0 A

Features

- Diffused Junction
- Ultra-Fast Switching for High Efficiency
- High Current Capability and Low Forward Voltage Drop
- Low Reverse Leakage Current
- Plastic Material: UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: DO-41 Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Marking: Type Number
- Weight: 0.35 grams (approx.)
- Mounting Position: Any



DO-41		
Dim	Min	Max
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	UF 100	UF 101	UF 102	UF 104	UF 106	UF 108	UF 1010	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	V	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @ T _A = 55°C	I _O	1.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load (JEDEC Method)	I _{FSM}	30							A
Forward Voltage @ I _F = 1.0A	V _{FM}	1.0		1.3		1.7			V
Peak Reverse Current at Rated DC Blocking Voltage @ T _A = 25°C @ T _A = 100°C	I _{RM}	5.0 100							μA
Reverse Recovery Time (Note 3)	t _{rr}	50			75				ns
Typical Junction Capacitance (Note 2)	C _j	20			10				pF
Typical Thermal Resistance Junction to Ambient	R _{θJA}	95							K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150							°C

- Notes:
1. Valid provided that leads are maintained at ambient temperature at a distance of 9.5mm from the case.
 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
 3. Measured with I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A. See figure 5.

RATING AND CHARACTERISTIC CURVES UF100 THRU UF1010

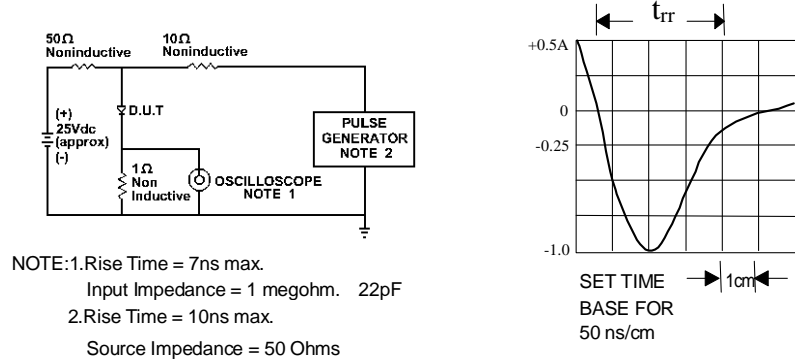


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

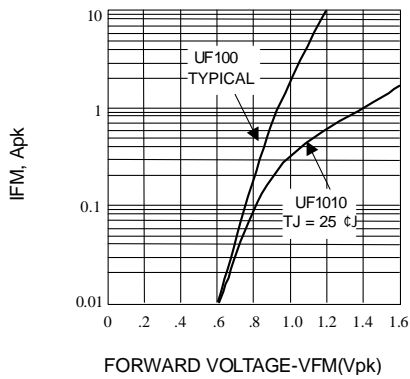


Fig. 2-FORWARD CHARACTERISTICS

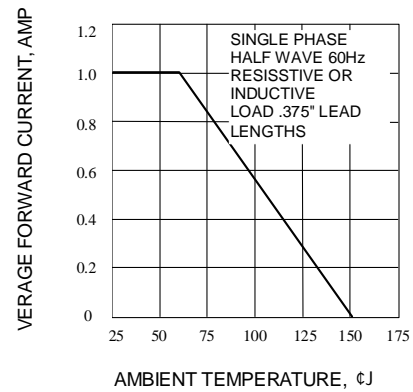


Fig. 3-FORWARD CURRENT DERATING CURVE

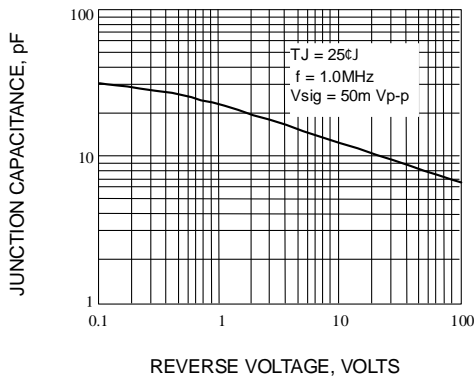


Fig. 4-TYPICAL JUNCTION CAPACITANCE

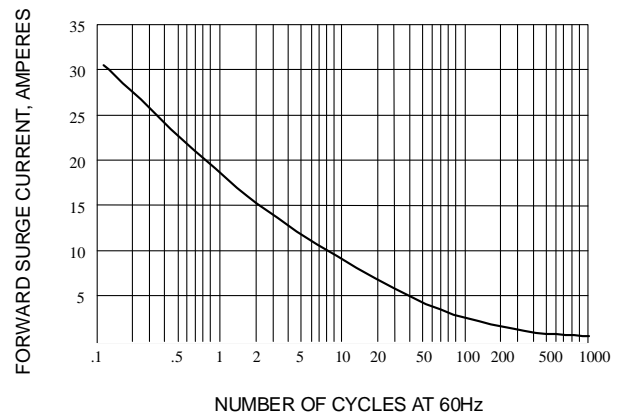


Fig. 5-PEAK FORWARD SURGE CURRENT