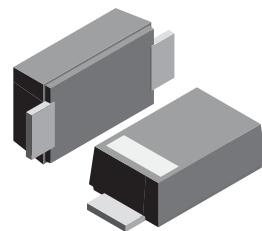


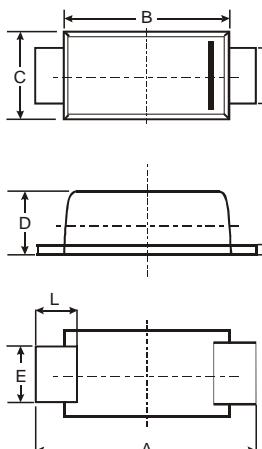
Features

- Low Forward Voltage Drop
- Guard Ring Construction for
- Transient Protection
- Negligible Reverse Recovery Time
- Very Low Reverse Capacitance



Mechanical Data

- Case: SOD-123FL
plastic body over passivated junction
- Terminals : Plated axial leads,
- solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.58	3.72	3.65
B	2.72	2.78	2.75
C	1.77	1.83	1.80
D	1.02	1.08	1.05
E	0.097	1.03	1.00
H	0.13	0.17	0.15
L	0.53	0.57	0.55

All Dimensions in mm

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	SD101AW	SD101BW	SD101CW	Unit
Peak Repetitive Reverse Voltage	V_{RRM}				
Working Peak Reverse Voltage	V_{RWM}	60	50	40	V
DC Blocking Voltage	V_R				
RMS Reverse Voltage	$V_{R(RMS)}$	42	35	28	V
Forward Continuous Current (Note 1)	I_{FM}		15		mA
Non-Repetitive Peak Forward Surge Current @ $t \leq 1.0\text{s}$ @ $t = 10\mu\text{s}$	I_{FSM}		50	2.0	mA
Power Dissipation (Note 1)	P_d		400		mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$		300		°C/W
Operating and Storage Temperature Range	T_j, T_{STG}		-65 to +125		°C

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition	
Reverse Breakdown Voltage (Note 2)	$SD101AW$ $SD101BW$ $SD101CW$	$V_{(BR)R}$	60 50 40	—	V	$I_R = 10\mu\text{A}$ $I_R = 10\mu\text{A}$ $I_R = 10\mu\text{A}$
Forward Voltage Drop (Note 2)	$SD101AW$ $SD101BW$ $SD101CW$ $SD101AW$ $SD101BW$ $SD101CW$	V_{FM}	—	0.41 0.40 0.39 1.00 0.95 0.90	V	$I_F = 1.0\text{mA}$ $I_F = 1.0\text{mA}$ $I_F = 1.0\text{mA}$ $I_F = 15\text{mA}$ $I_F = 15\text{mA}$ $I_F = 15\text{mA}$
Peak Reverse Current (Note 2)	$SD101AW$ $SD101BW$ $SD101CW$	I_{RM}	—	200	nA	$V_R = 50\text{V}$ $V_R = 40\text{V}$ $V_R = 30\text{V}$
Total Capacitance	$SD101AW$ $SD101BW$ $SD101CW$	C_T	—	2.0 2.1 2.2	pF	$V_R = 0\text{V}, f = 1.0\text{MHz}$
Reverse Recovery Time	t_{rr}	—	1.0	ns	$I_F = I_R = 5.0\text{mA}$, $I_{rr} = 0.1 \times I_R, R_L = 100\Omega$	

Notes: 1. Part mounted on FR-4 board with recommended pad layout
2. Short duration test pulse used to minimize self-heating effect.



SUNMATE

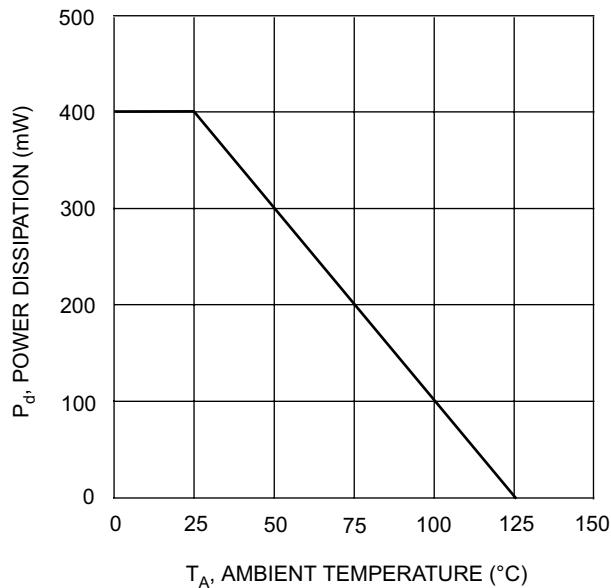


Fig.1 Power Derating Curve

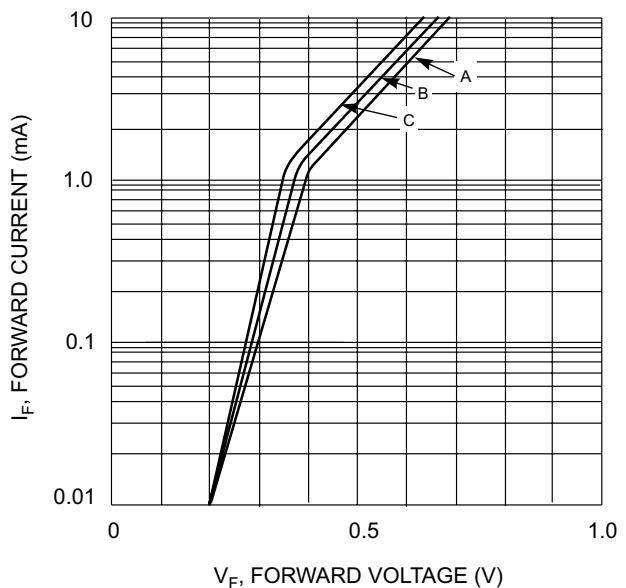


Fig. 2 Typical Forward Characteristic

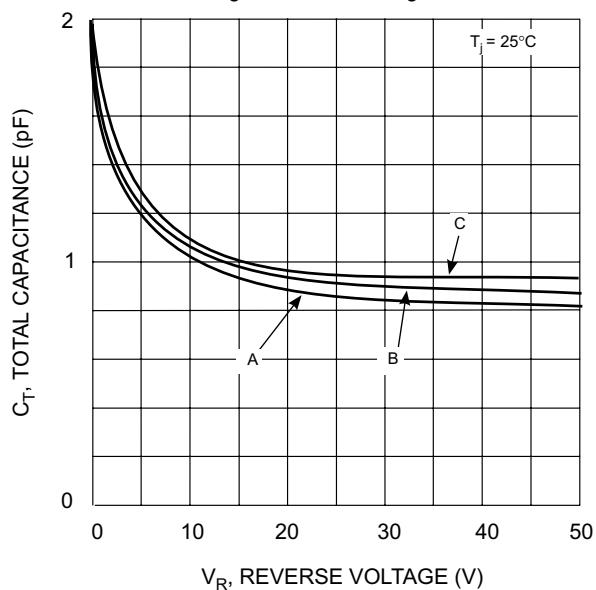


Fig. 3 Typ. Total Capacitance vs Reverse Voltage