

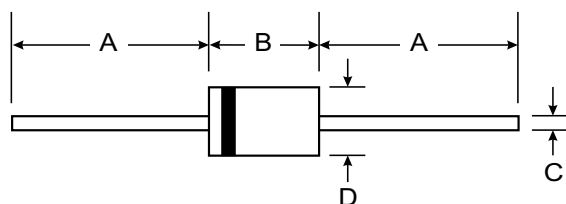
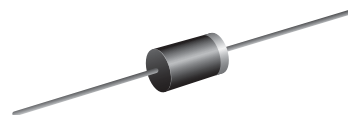
VOLTAGE RANGE: 100V
CURRENT: 2.0 A

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

Mechanical Data

- Case : DO-15 Molded plastic
- Epoxy : UL94V-O rate flame retardant
- Lead : Axial lead solderable per MIL-STD-202, Method 208 guaranteed
- Polarity : Color band denotes cathode end
- Mounting position : Any
- Weight : 0.465 gram



DO-15		
Dim	Min	Max
A	25.40	—
B	5.50	7.62
C	0.686	0.889
D	2.60	3.60
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SR2A0	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	100	Volts
Maximum RMS voltage	V _{RMS}	70	Volts
Maximum DC blocking voltage	V _{DC}	100	Volts
Maximum average forward rectified current 0.375" (9.5mm) lead length(see fig.1)	I _(AV)	2.0	Amp
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	60.0	Amps
Maximum instantaneous forward voltage at 2.0A	V _F	0.85	Volts
Maximum DC reverse current at rated DC blocking voltage	I _R	1.0 10.0	mA
Typical junction capacitance (NOTE 1)	C _J	80	pF
Typical thermal resistance (NOTE 2)	R _{qJA}	50.0	°C/W
Operating junction temperature range	T _J	-65 to +150	°C
Storage temperature range	T _{STG}	-65 to +150	°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2. Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted



AVERAGE FORWARD RECTIFIED CURRENT, AMPERES

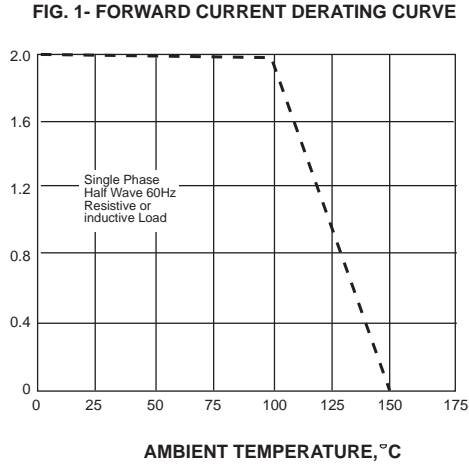


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

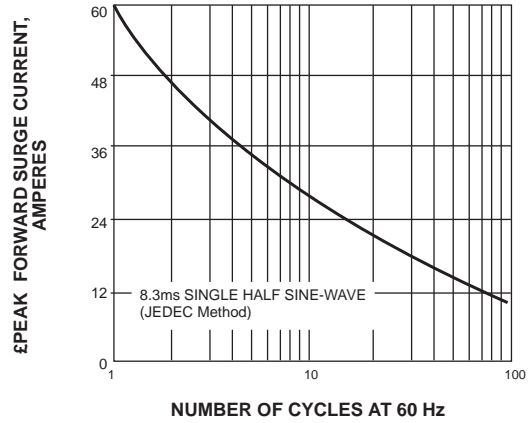


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

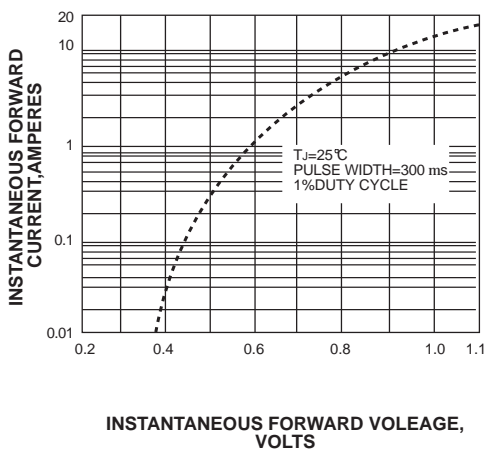


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

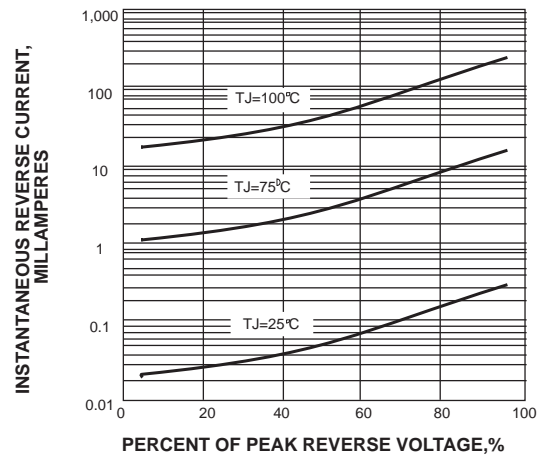


FIG. 5-TYPICAL JUNCTION CAPACITANCE

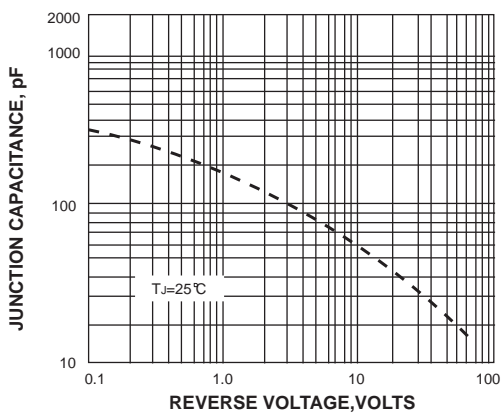


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE

