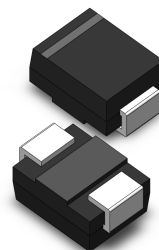


VOLTAGE RANGE: 90 - 100V
CURRENT: 2.0 A

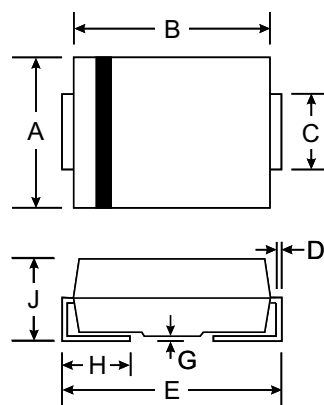
Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O



Mechanical Data

- Case: SMB/DO-214AA, Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.093 grams (approx.)



SMB(DO-214AA)		
Dim	Min	Max
A	3.30	3.94
B	4.06	4.70
C	1.91	2.21
D	0.15	0.31
E	5.00	5.59
G	0.10	0.20
H	0.76	1.52
J	2.00	2.62
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SS2H9	SS2H10	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	90	100	V
Working Peak Reverse Voltage	V_{RWM}			
DC Blocking Voltage	V_R			
RMS Reverse Voltage	$V_{R(RMS)}$	64	71	V
Average Rectified Output Current @ $T_L = 105^\circ\text{C}$	I_O	2.0		A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50		A
Forward Voltage @ $I_F = 2.0\text{A}$	V_{FM}	0.85		V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	I_{RM}	0.5	20	mA
Typical Thermal Resistance (Note 1)	$R_{\theta JL}$ $R_{\theta JA}$	17	75	$^\circ\text{C/W}$
Operating Temperature Range	T_j	-65 to +125		$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +150		$^\circ\text{C}$

Note: 1. Mounted on P.C. Board with 8.0mm² copper pad area.

RATINGS AND CHARACTERISTIC CURVES SS2H9 THRU SS2H10

