

SK52A - SK510A

SURFACE MOUNT SCHOTTKY BARRIER DIODES

VOLTAGE RANGE: 20 - 100V CURRENT: 5.0 A

Features

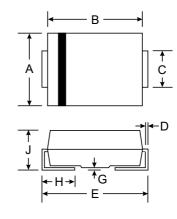
- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

Mechanical Data

- Case: SMA/DO-214AC, Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.064 grams (approx.)







SMA(DO-214AC)							
Dim	Min	Max					
Α	2.29	2.92					
В	4.00	4.60					
С	1.27	1.63					
D	0.15	0.31					
E	4.80	5.59					
G	0.10	0.20					
Н	0.76	1.52					
J	2.01	2.62					
All Dimensions in mm							

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

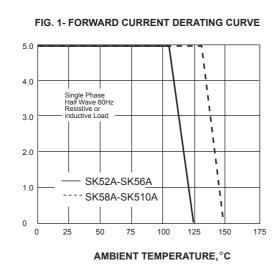
Characteristic	Symbol	SK52A	SK53A	SK54A	SK55A	SK56A	SK58A	SK510A	Unit
Maximum repetitive peak reverse voltage	VRRM	20	30	40	50	60	80	100	V
Maximum RMS voltage	VRMS	14	21	28	35	42	56	70	V
Maximum DC blocking voltage	VDC	20	30	40	50	60	80	100	V
Maximum average forward rectified current at TL(see fig.1)	l(AV)	5.0					Α		
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	150.0						Α	
Maximum instantaneous forward voltage at 5.0A	VF	0.55			0.70		0.85	0.85	
Maximum DC reverse current Ta=25℃ at rated DC blocking voltage Ta=100℃	lr	0.5					mA		
Typical junction capacitance (NOTE 1)	Cı	200						pF	
Typical thermal resistance (NOTE 2)	Reja	50.0					°C/W		
Operating junction temperature range	TJ,	-65 to +125 -65 to +150					°C		
Storage temperature range	Тѕтс	-65 to +150					°C		

Note:1.Measured at 1MHz and applied reverse voltage of 4.0V D.C. 2.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas

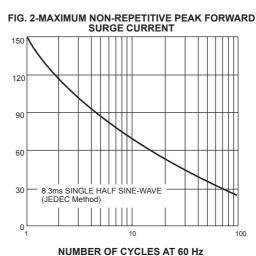


RATINGS AND CHARACTERISTIC CURVES SK52A THRU SK510A











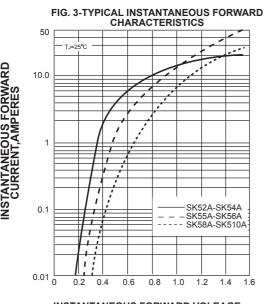


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

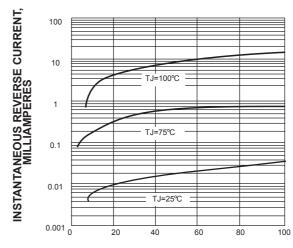
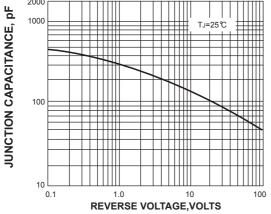


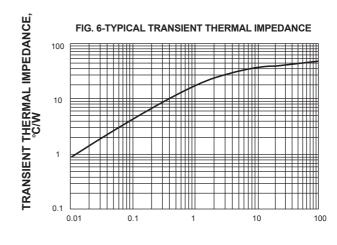


FIG. 5-TYPICAL JUNCTION CAPACITANCE









t,PULSE DURATION,sec.